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FORM PTO-1449	ATTY. DOCKET NO. 10971189-4	APPLICATION NO.	CONFIRMATION NO.
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	APPLICANT Arnold, et al.		
(Use several sheets if necessary)	FILING DATE 08/7/2003	GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
ξC	1A	5,859,999	01/12/1999	Morris, et al.	
ξG	1B	5,860,017	01/12/1999	Sharangpani, et al.	
ξC	1C	5,737,624	04/1998	Garg, et al.	
ξC	1D	4,903,196	02/1990	Pomerene, et al.	
ξC	1E	4,891,753	01/1990	Budde, et al.	
ξC	1F	6,266,766	07/2001	O'Conner, Dennis M.	
	1G				
	1H				
	1I				
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)
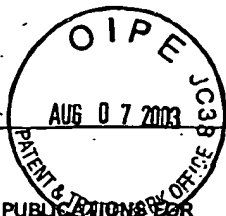
ξC	1Q	Alexander Wolfe, "Patents Shed Light on Merced: Techiniques of Predication and Speculation Detailed," Electronic Engineering Times, February 15, 1999, pp. 43-44.
ξC	1R	Patterson, et al., "Computer Architecture: A Quantitative Approach," Morgan Kaufmann Publishers, Inc., 2ed, pp. 150-193.
ξC	1S	Gary Lauterbach, "Sun's Next-Generation High-End SPARC Microprocessor Forum, October 14-15, 1997, pp. 3-6.

EXAMINER	DATE CONSIDERED 12/1/05

OIPE AUG 07 2003 JC38 PATENT & TRADEMARK OFFICE

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EC.	1Q	William J. Bowhill, et al., "Circuit Implementation of a 300-MHz 64-bit Second-generation CMOS Alpha CPU," Digital Technical Journal, Vol. 7 No. 1, 1995, pp. 100-114.
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EXAMINER	DATE CONSIDERED 12/1/05